## **Scholar Details:**

External Scholars							
S.No		Presently working Institution/ Industry	Provisional Registration Details				Present status of the PhD
	Candidate Name & Designation		Year of Reg.	Reg.No.	Title of the Research Work	No. of Paper Published in Annexure- I	(Doing coursework/ Comprehensive viva completed/ Synopsis submitted/ Thesis submitted)
1.	Mr.Silambarasan D	Anna University Regional Centre, Coimbatore	Jan 2015	1513469430	High speed residue system for cryptography applications		Comprehensive viva completed
2.	Maragatharaj S	Knowledge Institute of Technology Kakapalayam Chennai	Jan 2016	1613469289	Design of massive input multi output low power variable digital filter banks for ultra wide band spectrum sensing in cognitive radio systems.		Comprehensive viva completed
3.	Mr.Nelson Kingsley Joel.P AP/ECE	Sri Ranganathan Engineering College, Coimbatore.	July 2015	1514469960	Implementation of Efficient Algorithms for Removal of Noises in Digital Images		Comprehensive viva completed
4.	Mr.Lenin Raja R AP/ECE	Sri Vidhya College of Engg. & Tech., Virudhunagar.	Jan 2015	1514469152	An Enhanced switching technique for reducing power consumption using multi dynamic node		Doing Course work

5.	Mr.Nagarajan S	Tamilnadu	Jan 2015	1514469434	Design of	Comprehensive
	Assistant Engineer,	Electricity Board,			Bidirectional	viva completed
		Devanurpudur,			Switched Boost	
		Pollachi			Converter for Efficient	
					over Flow in AC/DC	
					Hybrid Microgrid	
6.	Mr.Senthilvel A	Tamilnadu	Jan 2015	1514469435	Efficient Design of	Comprehensive
	Assistant Executive	Electricity Board,			Solar Power Plant	viva completed
	Engineer (O&M)	Pollachi			Using Soft Computing	
					Techniques and FPGA	
7.	Ms.P.Gnanambikai,	NPTC,Pollachi.	06.06.2016	16244697154	Design of Efficient	Doing Course
	Lecturer(SG)/ECE				VLSI architecture for	work
					image De-Noising	